

Indian Statistical Institute
M.Tech.(CS), 2022-2023
Class Test 1 (March 31, 2023)

Subject: Operating Systems, Syllabus: Memory

Total: 10 marks

Duration: 1 hour

Answer all the questions

1. Let there be a process for which the base and limit registers hold the values x and y , respectively. Suppose the process is getting accessed in main memory through logical address space. Then for which of the following addresses it will NOT get a *trap* signal?

- (A) from x to $x + y$ (B) from x to $x + y + 1$ (C) from x to $x + y - 1$
(D) from $x + 1$ to $x + y$ (E) from $x + 1$ to $x + y + 1$ (F) from $x + 1$ to $x + y - 1$

2. Consider the following statements.

- S1: During compilation, absolute code is bound to actual physical addresses.
- S2: During loading, relocatable code is bound to relative addresses.
- S3: During execution, absolute code and relocatable code are bound together.

Which of the following statements is(are) correct?

- (A) S1 only (B) S2 only (C) S3 only
(D) S1 and S3 only (E) S2 and S3 only (F) S1, S2 and S3

3. Suppose 3 processes, labeled as p_1 , p_2 and p_3 , have arrived in the ready queue. Memory partitions are to be dynamically allocated to them. The memory requirements of p_1 , p_2 and p_3 are x kb, y kb and z kb, respectively. The list of free blocks, numbered as per their ordering in the memory, is as follows.

Block Number	1	2	3
Free Space	x kb	y kb	z kb

Let the free blocks are searched in the above list from left to right and allocated using the strategies first fit, best fit and worst fit. If allocations for all the strategies are exactly the same, then which of the following condition(s) necessarily hold(s)?

- (A) $x < y < z$ (B) $x > y > z$ (C) $x < y$ and $y > z$
(D) $x > y$ and $y < z$ (E) $x = y = z$ (F) None of the above

4. Consider an implementation of paging that accommodates the page number and offset number of the logical address in x and y number of bits, respectively. Then which of the following statements is(are) correct?

- (A) The page size is 2^y (B) The size of logical address space is $2^{(x+y)}$
(C) The maximum offset number is $2^{(y-1)}$ (D) The maximum page number is $2^{(x-1)}$

